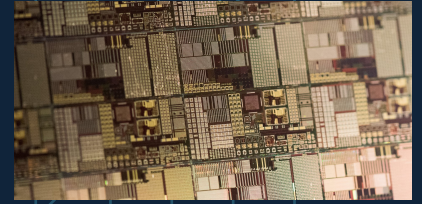


Workshop on Open-Source Tools for Analog and Mixed-Signal IC Design



Die photograph · Source: IHP

16 NOVEMBER 2026

09:30–12:15 | ISEL · Auditorium A | Lisbon

CALL FOR PARTICIPATION

Explore the tools and open PDKs making integrated-circuit design more accessible.

For students, researchers, young professionals and industry participants. Discover modern design methodologies, practical starting points and resources to continue learning after the workshop.

In focus Open-source toolchains and design flows · IHP OpenPDK initiative · Analog and digital PDK integration · Design-for-yield and manufacturing · Advanced semiconductor technologies

PROGRAMME

09:30	Welcome and participant reception
10:00	IHP OpenPDK initiative Dr. Wladek Grabinski · GMC Consulting
10:45	Expected impact of the OpenPDK initiative in education Prof. João Pedro Oliveira · NOVA School of Science and Technology (NOVA FCT)
11:30	Architectural evaluation of iterative and unrolled AES in 45 nm using an open-source flow Dinis Santos · Uninova Researcher
12:10	Upcoming events in open-source IC design · 5 minutes
12:15	Closing session

Organised by IEEE Portugal Section BT/CAS/CE Joint Chapter

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